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(Not for submission under 37 CFR 1.99)

Application Number	10765827
Filing Date	2004-01-26
First Named Inventor	Yolanta Beresnevichiene
Art Unit	2196
Examiner Name	CAO, DIEM K
Attorney Docket Number	200207542-2

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	2	Kumar, N., Childers, B. and Soffa, M.L., Low Overhead Program Monitoring and Profiling, Proceedings of the 6th ACM SIGPLAN-SIGSOFT Workshop on Program Analysis for Software Tools and Engineering, 2005, pp. 28-34.
	3	Scott, K., et al., Overhead Reduction Techniques for Software Dynamic Translation, Proceedings of 18th International Parallel and Distributed Processing Symposium [online], April 2004 [retrieved 2009-11-02]. Retrieved from Internet: <URL: http://cs.pitt.edu/~naveen/papers/hst2004.pdf > pp. 1-8.
	4	Kumar, N. and Childers, B., Flexible Instrumentation for Software Dynamic Translation, Workshop on Exploring the Trace Space, during the International Conference of Supercomputing [online], 2003 [retrieved 2009-11-02], Retrieved from Internet: <URL http://www.naveenkumar.org/papers/traces_workshop-03.pdf > pp. 1-10.
	5	http://rogue.colorado.edu/pin/index.html , Pin-A Dynamic Binary Instrumentation Tool, December 8, 2005, 1 page.
	6	Reddi et al., "Pin: A Binary Instrumentation Tool for Computer Architecture Research and Education," Workshop on Computer Architecture Education 2004 (WCAE-04), Munich, Germany, June 19, 2004, 8 pages.
	7	Luk et al., "Pin: Building Customized Program Analysis Tools with Dynamic Instrumentation," ACM SIGPLAN 2005 Conference on Programming Language Design and Implementation (PLDI), June 12-15, 2005, 11 pages.
	8	Patil et al., "Pinpointing Representative Portions of Large Intel Itanium Programs with Dynamic Instrumentation," 37th Annual IEEE/ACM International (MICRO-37 2004), 12 pages.
	9	Luk et al., "Pin: Building Program Analysis Tools with Dynamic Instrumentation, Intel Corporation," December 12, 2005, 15 pages.
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